

WE CLAIM:

Sub
al

1. A ~~driver circuit for driving signal lines of a matrix type display device,~~
2 comprising:
3 pulsewidth modulation circuitry for generating pulsewidth modulated video data;
4 and
5 driver circuitry for driving said signal lines in accordance with the pulsewidth
6 ~~modulated video data~~

1 2. The driver circuit according to claim 1, wherein said driver circuitry
2 comprises level-shifting circuits.

1 3. The driver circuitry according to claim 1, wherein said pulsewidth
2 modulation circuitry comprises a programmable logic array.

1 4. The driver circuitry according to claim 1, wherein said pulsewidth
2 modulation circuitry comprises an application specific integrated circuit.

1 5. The driver circuit according to claim 1, wherein said signal lines are
2 connected to emitter elements of a field emission display.

1 6. The driver circuit according to claim 1, wherein said pulsewidth
2 modulation circuitry generates the pulsewidth modulated video data based on RGB video
3 ~~data supplied thereto.~~

1 7. The driver circuit according to claim 1, wherein said driver circuitry is
2 provided on a chip other than a chip on which said pulsedwidth modulation circuitry is
3 provided.

1 8. The driver circuit according to claim 1, wherein said driver circuitry
2 comprises driver circuits that are loaded in parallel with the pulsedwidth modulated video
3 data.

Sub
a2

1 9. A matrix type display device comprising:
2 display elements connected to row lines and column lines; and
3 a driver circuit for driving said column lines, said driver circuit comprising:
4 pulsedwidth modulation circuitry for generating pulsedwidth modulated
5 video data; and
6 driver circuitry for driving said column lines in accordance with the
7 pulsedwidth modulated video data.

1 10. The matrix type display device according to claim 9, wherein said driver
2 circuitry comprises level-shifting circuitry. -

1 11. The matrix type display device according to claim 9, wherein said display
2 device is a field emission display device.

1 12. The matrix type display device according to claim 9, wherein said display
2 device is a plasma display device.

1 13. The matrix type display device according to claim 9, wherein said
2 pulsedwidth modulation circuitry comprises a programmable logic array.

1 14. The matrix type display device according to claim 9, wherein said
2 pulsedwidth modulation circuitry comprises an application specific integrated circuit.

1 15. The matrix type display device according to claim 9, wherein said
2 pulsedwidth modulation circuitry generates the pulsedwidth modulated video data based on
3 RGB video data supplied thereto.

1 16. The matrix type display device according to claim 9, wherein said driver
2 circuitry is provided on a chip other than a chip on which said pulsedwidth modulation
3 circuitry is provided.

1 17. The matrix type display device according to claim 9, wherein said driver
2 circuitry comprises driver circuits that are loaded in parallel with the pulsedwidth
3 modulated video data.

1 18. ~~A method of driving signal lines of a matrix type display device,~~
2 comprising:

3 generating pulsedwidth modulated video data; and
4 ~~driving said signal lines in accordance with the pulse-width modulated data.~~

1 19. The method according to claim 18, wherein said matrix type display
2 device is a field emission display device.

1 20. The method according to claim 18, wherein said matrix type display
2 device is a plasma display device.

1 21. The method according to claim 18, wherein the pulsedwidth modulated
2 video data is generated based on RGB video data.

Add
all
Add
B1